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## U.S. UTILITY Patent Application

M.S.	O.I.P.E.	PATENT DATE
SCANNED TR4	Q.A.	

APPLICATION NO. 09/893598	CONT/PRIOR F	CLASS <del>257</del> 345	SUBCLASS 211	ART UNIT <del>2841</del> 2675	EXAMINER BELL
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APPLICANTS Yoshinori Uchiyama

TITLE Semiconductor circuit in which power consumption is reduced and semiconductor circuit system using the same

PTO-2040  
12/88

### ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION							

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	_____ (Primary Examiner) _____ (Date)			<b>ISSUE FEE</b>	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) _____ (Date)			<b>ISSUE BATCH NUMBER</b>	

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